

Multi Channel Stop and Wait ARQ Communication Method and Apparatus

References to Related Application(s)

This application is related to provisional US Application No. 60/173,694; which is incorporated by reference herein.

Related Field of the Invention

The invention relates to the filed of communications, and more particularly, to data communications.

Background of the invention

Automatic repeat request (ARQ) schemes are commonly used in data communication to provide a more efficient communication between a source user and a destination user. Several types of ARQ, such as hybrid ARQ, stop-and-wait ARQ, go-back-N (GBN) ARQ and selective repeat (SR) ARQ are available where each provides an efficient use of the communication resources between a source user and a destination user. However, each of the prior arts schemes suffers from at least one problem that makes the communication system more complex, more expensive or inefficient for data communication.

For example, in case of hybrid ARQ, the destination user stores soft versions of the previously sent copies of data packet for soft combining. The data packet with possibly additional incremental redundancy in the channel is sent which increases the coding rate and effectively lowering the communication data rate. The destination user may effectively soft-combine the soft copy stored in the memory with the newly arrived soft copy to decode the data in the data packet. The communication system resources may remain idle between the previous and the new transmission times. The complexity of the communication system as a result is increased due to requirements of additional control, code construction, decoder implementation, and memory requirements.

In case of selective repeat ARQ, source user transmits data packets with a sequence number to be decoded by the destination user. If a data packet arrives with error at the destination user, the destination user sends a message to the source user

for re-transmission of the packet of data while identifying the data packet sequence number. Depending on the length of delay of such feedback to the source user, the maximum sequence number is increased to allow for the length of the feedback delay. As such, the destination user, for example a mobile station, may be required to have a large memory segment to store transmission of packets of data until all data packets including the packet with the maximum sequence number have arrived without error. The number of data packets stored at the destination user may increase substantially which places a high burden on the memory requirements.

In case of stop-and-wait ARQ, the source user transmits only one packet of data and waits until it receives an indication of successful reception at the destination user, then the source user transmits another data packet. The communication channel remains idle while the source is waiting for the acknowledgment. As such, low usage of the available communication bandwidth resulting in an inefficient communication is a problem associated with stop-and-wait.

Hybrid ARQ may be coupled with stop and wait scheme, however, the result does not overcome the idle communication channel problem experience while waiting for the acknowledgment. Hybrid ARQ may also be coupled with selective repeat scheme; however, the result is not without the known problems. In fact, when hybrid ARQ is coupled with the selective repeat scheme, the requirement of the memory size is substantially increased because in addition to the storing at least one copy of the data packets, additional copies may need to be stored for soft combining operation of the hybrid ARQ portion.

Therefore, there is a need for a method and apparatus of ARQ which provides efficient use of the communication resources without the known complexity of the prior arts.

Brief Description of the Drawings

FIG. 1 depicts a communication system employing various aspects of the invention.

FIG. 2 is a block diagram of a system consisting of a single source and destination over a slotted data channel in accordance with the preferred embodiment of the present invention.

FIG. 3 is a block diagram of a system consisting of multiple sources and destinations over a slotted data channel in accordance with the preferred embodiment of the present invention.

a time frame 123, transmission of data packet 111 is repeated in time frame 123 which is immediately subsequent to time frame 112. Similarly, if acknowledgement associated data packets 112 has not arrived before transmitting a time frame 124, transmission of data packet 112 is repeated in time frame 124. The transmission sequence of data packets 111 and 112 is repeated until arrival of an acknowledgement associated with either data packets 111 or 112. In all cases above describing a repeat of data packet, it is possible to substitute an associated packet constructed entirely of parity information or an alternate combination of information and parity. This substitution represents an alternate form of Hybrid ARQ known as Incremental Redundancy.

After the acknowledgment of acceptable reception to either data packet 111 or 112, source user terminates transmission of its associated data packet. Source user 101 transmits a third data packet in substitute of the terminated data packet in the sequence of the first and second time frames.

The sequence of the first and second time frames may be consecutively odd and even numbered time frames in numbered time frames such as time frame sequence 190 in a time division multiple access communication system. If time frame 121 is numbered as “n”, an even number time frame, time frame 122 is then an odd numbered time frame, time frame “n+1”. Similarly time frame 123, time frame “n+2”, is an even numbered time frame, and time frame 124, time frame “n+3”, an odd numbered time frame, and so on. The first time frame may be referred to as an odd numbered channel and second time frames as an even numbered channel immediately subsequent to the odd numbered channel in a time division multiple access communication system.

If data packet 111 was transmitted on an even numbered time frame and data packet 112 on an odd numbered time frame of sequence of time frames 190, transmission of data packets 111 and 112 on even and odd numbered time frames continues until an acknowledgement associated with either data packet 111 or 112 is detected at source user 101. For example, if the acknowledgement is associated with data packet 112, a third data packet is selected for transmission in substitute of the data packet 112. As such, the third data packet is transmitted on the odd time frames, and the data packet 111 on the even time frames of sequence of time frames 190.

The first and second data packets may be transmitted for a same destination user or a first and second destination users. For example, in down link 180, data packets 111 and 112 may be destined for a single destination user such as any of the destination users 151-54. In another situation, the data packets 111 and 112 may be destined for respectively destination users 151 and 152, for example. Similarly, any substituted data packet may be for the same or different destination users.

In case the downlink received by a destination user is in poor condition, an acknowledgement may not be transmitted from the destination user for some time. During this time, the data packets destined for such a destination user may be repeated many times in the first and second time frames. To avoid unnecessary usage of the communication resources in the down link 180, source user 101 limits transmission of the data packet to a predetermined number of repetitions.

Source user 101, along with other blocks such as an encoder (not shown), may include a queue buffer 102 for buffering data packets for transmission. A channel sequencer 103 retrieves the first and second data packets from queue buffer 102 and aligns the first and second data packets in sequence to be received by a transmitter 104 for transmission from an antenna 105 from source user 101. Queue buffer 102 may buffer data packets according to a transmission priority of the first and second data packets. In case of transmitting a third data packet, the first, second and third data packets are queued in buffer for transmission from the source user according to a transmission priority. The third data packet may be selected from a plurality of data packets in queue buffer 102 based on a transmission priority when being substituted for either the first or the second data packet in the sequence of the first and second (i.e. even and odd or odd and even) time frames.

To perform hybrid ARQ portion, the destination users combines correspondingly soft copies of the repeated transmission of data packets for decoding the data in the data packet. Once a data packet satisfactorily is decoded via soft combining, the destination user generates and transmits an acknowledgment of acceptable reception of data packet. As such, when the first and second data packets are destined for the same destination user as a mobile station, the mobile station is required to have a memory buffer for storing the first and second data packets upon arrival. In case of soft combining, soft copies of the first and second data packets may need to be stored. This is a substantial reduction in memory requirement in a mobile station operating in a communication system relative to alternative ARQ schemes according to various aspects of the invention.

In case the acknowledgment of acceptable repetition of a data packet can not arrive within a time frame, a time in units of a finite time period is determined when at a source user a feedback acknowledgment can arrive acknowledging acceptable reception of a data packet at a destination user. The finite time period may equal to duration of time frame. A source user transmits multiple data packets in a sequence of multiple time frames equal to the determined units of time frames. While waiting for detecting an acknowledgment of acceptable reception of data packet associated with either one of the multiple data packets, transmission of the multiple of data packets in the sequence of multiple time frames is repeated. The time may be approximately an

When used in a Code Division Multiple Access (CDMA) system, it is possible to send multiple packets in the even period and multiple packets in the odd period. When anyone of the multiple packets is acknowledged it may be replaced independently of all the other packets in the manner described previously. Various aspects of the invention may be implemented by way of software or hardware implementations. Use of such methods is well known in the art. The source user may be a base station and the destination user may be mobile stations in a cellular communication system. The source user and destination user may also employ an encoding and decoding apparatus known in the art.

Dual channel Stop-and-Wait ARQ offers a solution by parallelizing the stop-and-wait protocol and in effect running a separate instantiation of the ARQ protocol in the idle channels. As a result no system capacity goes wasted since one instance of the algorithm communicates a data block on the forward link at the same time that the other communicates an acknowledgment on the reverse link.

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sequencer to admit data blocks to either the even or odd transmitter. Once admitted, each transmitter performs a conventional stop-and-wait ARQ algorithm in its respective even or odd timeslot by transmitting the data block on the data channel and sequence bit on the associated control channel. Similar to the source, the destination device contains both an odd and even receiver receiving blocks from the respective even and odd timeslots (a.k.a. 5 ms frames). Each receiver is coupled with an independent hybrid ARQ decoder. The hybrid ARQ decoder signals the success (or failure) of the data block on a separate feedback channel. The hybrid ARQ decoder will store all symbols from unsuccessful attempts for the current sequence number. At most, one even set of symbols and one odd set of symbols are stored by the even and odd hybrid ARQ decoder, respectively. Independent feedback channels exist to support each instance of the stop-and-wait. The feedback channel is scheduled on the reserve link in the frame immediately following the forward link transmission.

FIG. 3 extends the model described in FIG. 2 to multiple sources and destinations. The source and destination devices are identical to Figure and are coupled by independent control and feedback channels. Unlike FIG. 2, all source and destination pairs share one data channel. Therefore, a system scheduler is required to arbitrate between the multiple sources in the system. The scheduler will select which source owns the current timeslot based on the status of each individual queue or possibly one large combined queue. In this case, the scheduler must communicate the ownership of the timeslot to the destination over the F-DCH in addition to the channel state, even or odd. In all other aspects, the system performs as in the single user case.

The dual channel stop and wait protocol operates over the F-SCH channel associated with F-DCH. The F-SCH carries the bearer data and is shared among multiple destination devices and will be decoded using the hybrid ARQ process. The F-DCH carries all the per-user control information such as the Dual Channel Identifier (DCHI), Sequence Number (SN), Abort Indication Aid (AIA), F-SCH Assignment and MSC identifier. Table 1 list control fields carried on the F-DCH.

Name	Size (bits)	Description	Function
DCHI	1	Dual Channel Indicator	Identifies current frame as belonging to either the even or odd instance of the stop-and-wait protocol.
SN	1	Sequence Number	The sequence number of the current frame taking on a value of either 0 or 1. SN is unique to the instance of stop-and-wait ARQ.
F-SCH-A	8	F-SCH Assignment	Identifies the Walsh-Hadamard codes assigned to the current user.
MCS	3	Modulation and Coding Scheme	Describes the modulation and coding scheme as specified in Table 2.

Table 1 Control Fields for Dual-Channel Hybrid ARQ.

Operation of the dual channel receiver's is illustrated in FIG. 4. The source will identify a packet on the F-SCH destined for the destination device by sending the control information in Table 1 over the F-DCH simultaneously. The F-SCH-A and MCS fields identify the bearer symbols and format assigned to the destination device. The DCHI field identifies the packet as belonging to either even or odd instance of the Hybrid ARQ control. Each even and odd instantiation will maintain an independent set of state variables and memory to store previous attempts. The local state variables include a current SN. The memory will store soft information for previous attempts of the current SN that has not been successfully decoded. After selecting an ARQ instance, the receiver checks the SN field. A SN field that does not match current SN suggests that the previous acknowledgement was lost, therefore, the receiver resends the ACK and discards the current data samples. When the SN field matches the current SN, the receiver merges the current frame's symbols with the symbols of previous attempts if they exist. The receiver then tries to decode the aggregate symbols. If successfully decoded, the receiver sends an acknowledgement to the source, passes decoded data to a higher layer, discards all stored symbols from memory and increments the current SN. Otherwise, the receiver takes no action and waits for the next attempt.

Modulation and Coding Selection Scheme for Forward Shared Channel

The Modulation and Coding Scheme (MCS) for Forward Shared Channel uses QPSK, 16 QAM and 64 QAM modulation with three basic Turbo coding rates (R) of

1, $\frac{3}{4}$ and $\frac{1}{2}$ respectively. The MCS operates in conjunction with Hybrid ARQ. The initial MCS for each IP packet is selected based on average link quality. Table 2 provides a list of seven (7) MCS schemes used for the Forward Shared Channel.

MCS	Modulation	Code Rate
7	64	1
6	64	$\frac{3}{4}$
5	64	$\frac{1}{2}$
4	16	$\frac{3}{4}$
3	16	$\frac{1}{2}$
2	4	$\frac{3}{4}$
1	4	$\frac{1}{2}$

Table 2 Modulation and Coding Schemes (MCS)

While the invention has been particularly shown and described with reference to a particular embodiment, it will be understood by those skilled in the art that various changes in form and details may be made therein without departing from the spirit and scope of the invention. It is intended that such changes come within the scope of the following claims.

What is claimed is: